

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/729,194	12/05/2003	Qi Xiang	039153-5002 (G0166)	2402	
34083	7590 06/22/2005		EXAM	EXAMINER	
AMD-MKE C/O FOLEY LARDNER			NGUYEN, KHIEM D		
	SCONSIN AVENUE E, WI 53202-5367		ART UNIT	PAPER NUMBER	
	,		2823		
			DATE MAILED: 06/22/2009	DATE MAILED: 06/22/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	
	10/729,194	XIANG, QI	(cm)
Office Action Summary	Examiner	Art Unit	
	Khiem D. Nguyen	2823	
The MAILING DATE of this communication a Period for Reply	appears on the cover sheet wit	th the correspondence ad	dress
A SHORTENED STATUTORY PERIOD FOR REF THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a r - If NO period for reply is specified above, the maximum statutory peri - Failure to reply within the set or extended period for reply will, by star Any reply received by the Office later than three months after the ma earned patent term adjustment. See 37 CFR 1.704(b).	N. 1.136(a). In no event, however, may a resepty within the statutory minimum of thirty od will apply and will expire SIX (6) MON tute, cause the application to become AB.	eply be timely filed y (30) days will be considered timely THS from the mailing date of this co ANDONED (35 U.S.C. § 133).	
Status			
1) Responsive to communication(s) filed on 18 2a) This action is FINAL . 2b) This action is FINAL . 2b) This action is in condition for allow closed in accordance with the practice under the pract	his action is non-final. vance except for formal matte	• •	emerits is
Disposition of Claims			
4) ☐ Claim(s) 12-19 and 22-34 is/are pending in the day of the above claim(s) is/are withd 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 12-19 and 22-34 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and application Papers.	rawn from consideration.		
Application Papers			
9) ☐ The specification is objected to by the Exami 10) ☑ The drawing(s) filed on <u>05 December 2003</u> is Applicant may not request that any objection to the Replacement drawing sheet(s) including the corr 11) ☐ The oath or declaration is objected to by the	s/are: a)⊠ accepted or b)□ he drawing(s) be held in abeyan ection is required if the drawing(ce. See 37 CFR 1.85(a). s) is objected to. See 37 CF	FR 1.121(d).
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for forei a) All b) Some * c) None of: 1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the priority docume application from the International Bure * See the attached detailed Office action for a li	ents have been received. ents have been received in Apriority documents have been eau (PCT Rule 17.2(a)).	oplication No received in this National	Stage
Attachment(s)	_		
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/0 Paper No(s)/Mail Date 	Paper No(s	ummary (PTO-413))/Mail Date formal Patent Application (PTC)-152)

Art Unit: 2823

DETAILED ACTION

Election/Restrictions

Applicant's election without traverse of Group II, claims 12-19 in the reply filed on April 18th, 2005 is acknowledged. The non-elected claims 1-11 and 20-21 requested to be cancelled without prejudice by the Applicant. Claims 22-34 are being added. Thus, claims 12-19 and 22-34 are now pending in this application.

Claim Rejections - 35 USC § 102

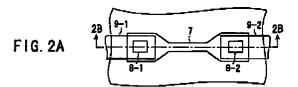
The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

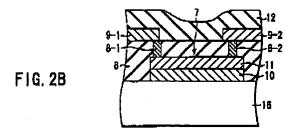
Claims 12-19, 22-25, 28-30 and 33-34 are rejected under 35 U.S.C. 102(e) as being anticipated by Toyoshima (U.S. Patent 6,703,680).

In re claim 12, <u>Toyoshima</u> discloses a fuse for an integrated circuit, the fuse comprising a material 11 capable of existing in a first phase (NiSi) or a second phase (NiSi₂) in response to at least one of a current signal and a voltage signal, the fuse having a different resistance in the first phase (20 $\mu\Omega$ cm) than in the second phase (60 $\mu\Omega$ cm) (col. 3, lines 15-56 and FIGS. 2A-B).



Art Unit: 2823

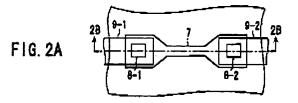
In re claim 13, <u>Toyoshima</u> discloses that the fuse further comprises a layer of material including silicon 10 and a silicide layer 11 (col. 3, lines 19-45 and FIG. 2B).



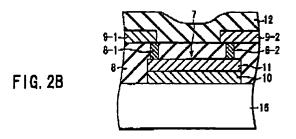
In re claim 14, <u>Toyoshima</u> discloses that the silicide 11 includes nickel (NiSi) (col. 3, lines 19-45).

In re claim 15, <u>Toyoshima</u> discloses that the first phase includes mononickel silicide (NiSi) and the second phase includes nickel disilicide (NiSi₂) (col. 3, lines 35-45).

In re claim 16, <u>Toyoshima</u> discloses an integrated circuit comprising: a polysilicon layer 10 disposed above an insulative structure 16; and a silicide layer 11 disposed above the polysilicon layer 10, the silicide layer 11 being a first type (NiSi) and being convertible to a silicide layer of a second type (NiSi₂) in response to a signal, wherein a resistance of the silicide layer changes when the silicide layer is converted from the first type (20 $\mu\Omega$ cm) to the second type (60 $\mu\Omega$ cm) (col. 3, lines 35-56 and FIGS. 2A-B).



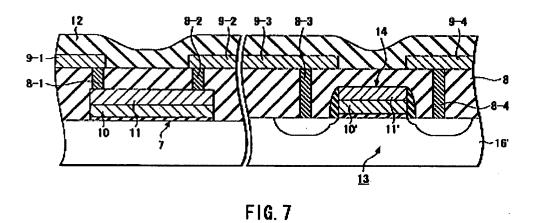
Art Unit: 2823



In re claim 17, <u>Toyoshima</u> discloses that the silicide layer of the first type is mononickel silicide (NiSi) (col. 3, lines 35-45).

In re claim 18, <u>Toyoshima</u> discloses that the silicide layer of the second type is nickel disilicide (NiSi₂) (col. 3, lines 35-45).

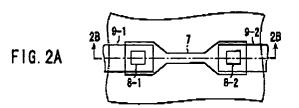
In re claim 19, <u>Toyoshima</u> discloses that the insulative structure is a field oxide or an insulative layer (FIG. 7).

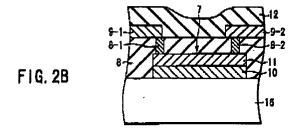


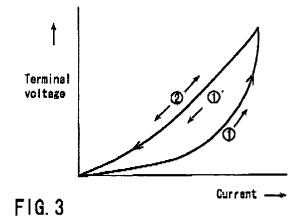
In re claim 22, <u>Toyoshima</u> discloses a fuse comprises: means for having a first phase (NiSi) and a second phase (NiSi₂), the first phase (20 $\mu\Omega$ cm) having a different resistivity than the second phase (60 $\mu\Omega$ cm) (col. 3, lines 35-45); and means for receiving

Art Unit: 2823

a current and changing the means for having from the first phase to the second phase with the current (col. 3, line 46 to col. 4, line 8 and FIGS. 2A-3).







In re claim 23, <u>Toyoshima</u> discloses that the second phase (60 $\mu\Omega$ cm) is a relatively higher resistance than the first phase (20 $\mu\Omega$ cm) (col. 3, lines 35-45).

In re claim 24, <u>Toyoshima</u> discloses that the current is a programming current (col. 3, line 46 to col. 4, line 8).

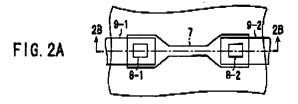
In re claim 25, <u>Toyoshima</u> discloses that the means for having is a material having a first sheet resistance in the second phase of at least two times of a second sheet resistance in the second phase (col. 3, lines 35-45).

In re claim 28, <u>Toyoshima</u> discloses that the means for having is a material including nickel (NiSi) (col. 3, lines 35-45).

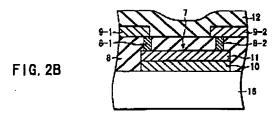
In re claim 29, <u>Toyoshima</u> discloses that the material is a silicide (NiSi) (col. 3, lines 35-45).

In re claim 30, <u>Toyoshima</u> discloses that first phase includes mononickel silicide (NiSi) and the second phase includes nickel disilicide (NiSi₂) (col. 3, lines 35-45).

In re claim 33, <u>Toyoshima</u> discloses that a fuse for an integrated circuit, the fuse comprising: a silicide layer (NiSi) 11; and a layer including silicon 10, the layer including silicon being above a bulk silicon substrate 16 or a field oxide structure and below the silicide layer, wherein the silicide layer is configured in a fuse pattern, wherein the silicide layer 11 is in a first phase (NiSi), the first phase being convertible to a second phase (NiSi₂), the first phase (20 $\mu\Omega$ cm) having a different resistance characteristic than the second phase (60 $\mu\Omega$ cm) (col. 3, lines 15-56 and FIGS. 2A-B).



Art Unit: 2823



In re claim 34, <u>Toyoshima</u> discloses that the fuse of claim 33 further comprising: conductive vias 8-1, 8-2 at a first end and a second end of the fuse pattern (col. 3, lines 19-34 and FIGS. 2A-B).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 26-27 and 31-32 are rejected under 35 U.S.C. 103(a) as being unpatentable over Toyoshima (U.S. Patent 6,703,680).

In re claims 26-27, <u>Toyoshima</u> discloses a first sheet resistance that is several times the second sheet resistance (col. 3, lines 19-56) but does not explicitly disclose that the first sheet resistance is at least 8 times the second sheet resistance as recited in claim 26, and approximately 10 times the second sheet resistance as recited in claim 27.

However, there is no evidence indicating the ranges of the first and second sheet resistance is critical and it has been held that it is not inventive to discover the optimum or workable range of a result-effective variable within given prior art conditions by routine experimentation. See MPEP § 2144.05. Note that the specification contains no

disclosure of either the critical nature of the claimed dimensions of any unexpected results arising there from. Where patentability is aid to be based upon particular chosen dimensions or upon another variable recited in a claim, the Applicant must show that the chosen dimensions are critical. <u>In re Woodruff</u>, 919 F.2d 1575, 1578, 16 USPQ2d 1934, 1936 (Fed. Cir. 1990).

In re claims 31-32, <u>Toyoshima</u> does not explicitly disclose that the first phase has a sheet resistance between 1-5 ohms per square and the second phase has a sheet resistance between 10 and 40 ohms per square.

However, there is no evidence indicating the ranges of the sheet resistance of the first and second phase is critical and it has been held that it is not inventive to discover the optimum or workable range of a result-effective variable within given prior art conditions by routine experimentation. See MPEP § 2144.05. Note that the specification contains no disclosure of either the critical nature of the claimed dimensions of any unexpected results arising there from. Where patentability is aid to be based upon particular chosen dimensions or upon another variable recited in a claim, the Applicant must show that the chosen dimensions are critical. In re Woodruff, 919 F.2d 1575, 1578, 16 USPQ2d 1934, 1936 (Fed. Cir. 1990).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khiem D. Nguyen whose telephone number is (571) 272-1865. The examiner can normally be reached on Monday-Friday (8:30 AM - 5:30 PM).

Art Unit: 2823

Page 9

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Olik Chaudhuri can be reached on (571) 272-1855. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

K.N. June 17th, 2005



W. DAVID COLEMAN PRIMARY EXAMINED